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Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(use as many sheets as necessary)</i>				<b>Complete if Known</b>	
				Applicant Number	Not yet assigned
				Filing Date	Herewith
				First Named Inventor	KOERTZEN, Henry W.
				Group Art Unit	Not yet assigned
				Examiner Name	Not yet assigned
Sheet	2	of	2	INTEL Docket Number	P17683

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
JKT	1	Huang, Wenkang et al.: "A Scalable Multiphase Buck Converter with Average Current Share Bus", International Rectifier, APEC 2003, 7pp.	
JKT	2	Huang, Wenkang et al.: "System Accuracy Analysis of the Multiphase Voltage Regulator Module", International Rectifier, APEC 2003, 7pp.	
JKT	3	"Intel Mobile Voltage Positioning Technology Overview", August 31, 1999, 5pp.	
JKT	4	Dettwiler, Steven, "Keeping Transients and Static in Check", Intel® DeveloperUPDATE Magazine, April 2001, 5pp.	
JKT	5	Linfinity Application Note AN-10, "Design Procedure for Microprocessor Buck Regulators", Copyright © 1998, Rev. 0.2.1. 07/98, 13pp.	
JKT	6	"Intel® Pentium® 4 Processor VR-Down Design Guidelines", November 2002, Order Number 249891-004, 16pp.	
JKT	7	Intel® APP-933, Application Note, "Flexible Motherboard Power Distribution and Control for the Pentium® III Xeon™ Processor", January 2000, Order Number 245245-002, 25pp.	
JKT	8	Freeman, Dave et al. "Mobile Devices: RF demands shrink power window", May 16, 2001, CommsDesign, download from <a href="http://www.commsdesign.com/printableArticle?doc_id=OEG20010516S0008">http://www.commsdesign.com/printableArticle?doc_id=OEG20010516S0008</a> on 11/20/2003, Copyright © 2003, CMP Media, LLC.	

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